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CONFIRMATION NO. 2504

SERIAL NUMBER 10/766,954	FILING OR 371(c) DATE 01/30/2004 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 61282-056
APPLICANTS Kenichi Tajika, Kyoto, JAPAN; Hiroki Tomoshige, Kanagawa, JAPAN; Minoru Itoh, Shiga, JAPAN; <i>None NL</i> ** CONTINUING DATA ***** <i>Yes NL</i> ** FOREIGN APPLICATIONS ***** JAPAN P2003-024093 01/31/2003 JAPAN P2003-313917 09/05/2003				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/14/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and <i>None</i> Acknowledged <i>None</i> Examiner's Signature <i>None</i> Initials <i>NL</i>		STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 16/15 INDEPENDENT CLAIMS 2
ADDRESS 20277				
TITLE Clock delay adjusting method of semiconductor integrated circuit device and semiconductor integrated circuit device formed by the method				
FILING FEE RECEIVED 1352	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	